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How Verification at Arm can kickstart your career

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Who am I?



<https://www.york.ac.uk/staff/support/mediation/>



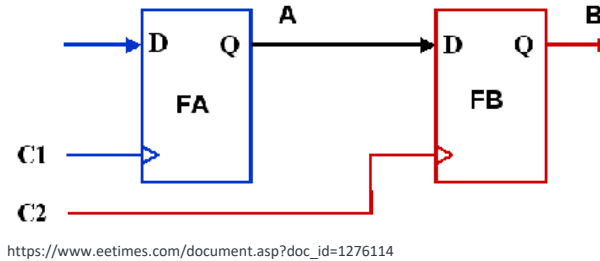
<https://www.archdaily.com/931958/arm-holdings-headquarters-scott-brownrigg>



<https://www.eu.socio.next.com/company/locations.html>

My first year at Arm

- + Investigating the use of Formal to solve Clock Domain Crossing (CDC) problems



- + Developing shared Formal scripts that are used across Arm
- + Using Formal to validate the Arm Architecture
 - Verification vs Validation

The following years at Arm, and what I learnt

+ Work in simulation and Core Testbench

- SystemVerilog, Python, C++
- New microarchitecture to learn
- Bigger team, with a clear deliverable

+ RAS Verification IP

- Leadership, managing small team of graduates
- Cross site working
- New section of the Arm Architecture

**Arm[®] Architecture Reference Manual
Supplement
Reliability, Availability, and Serviceability (RAS), for Armv8-A**

Summary

- + Verification as an early career choice exposes you to many aspects of Engineering
- + Verification can help to develop a varied skillset
- + Working with the Verification teams at Arm gives a real sense of accomplishment

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Thank You

Danke

Gracias

Grazie

谢谢

ありがとう

Asante

Merci

감사합니다

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